



PTO/SB/08A (10-01)

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U.S. PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
CF		US 2001/0014927 A1	8-16-2001	Chang et al.	
CF		US 6,272,563 B1	8-7-2001	Ajanovic et al.	
CF		US 6,269,156 B1	7-31-2001	Matt et al.	
CF		US 6,269,103 B1	7-31-2001	Laturell	
CF		US 6,256,697 B1	7-3-2001	Ajanovic et al.	
CF		US 6,216,170 B1	4-10-2001	Giovannoni et al.	
CF		US 6,175,884 B1	1-16-2001	Harriman et al.	
CF		US 6,145,039	11-7-2000	Ajanovic et al.	
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CF		US 6,072,783	6-6-2000	Riley	
CF		US 6,058,427	5-2-2000	Viswanath et al.	
CF		US 6,016,311	1-18-2000	Gilbert et al.	
CF		US 5,970,066	10-19-1999	Lowry et al.	
CF		US 5,960,036	9-28-1999	Johnson et al.	
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CF		US 5,852,631	12-22-1998	Scott	
CF		US 5,835,787	11-10-1998	Raffman et al.	
CF		US 5,724,413	3-3-1998	Suffern et al.	
CF		US 5,475,691	12-12-1995	Chapman et al.	
CF		US 5,297,231	3-22-1994	Miller	

FOREIGN PATENT DOCUMENTS

Examiner Signature	Chieh Min T	Date Considered	3/20/03
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PTO/SB/08B (10-01)

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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
<i>CV</i>		ALCATEL, Data Sheet for MTC-20144, "Integrated ADSL CMOS Analog Front-End Circuit," Rev. 1 - December 1998, pp. 1-24.	

Examiner Signature	Chris Mo Jr	Date Considered	3/20/03
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